Release notes for Altium Designer 10 update (10.771.23139)

Additional Resources

[Complete list of Altium Designer updates]

Updated plug-ins from release 10.747.23074 to 10.771.23139

Date: 23 November 2011

System Components: Altium Designer Base

5177	Improved performance of "Find Partial Matches" stage of document comparator
5530	Detection of duplicate designator and sub-part violations during scrap compile has been fixed. View BugCrunch report #606.
5593	The ECO dialog no longer warns about errors if they have all been suppressed
5751	Fixed crash that occurred when right-clicking an empty messages panel. View BugCrunch report #1174.

System Components: PCB System

4510	PCB Editor's ECO process now performs net assignment changes for all same-name component pads. View BugCrunch report #146.
4681	The interactive routing tools now honor the "Routing Layers" rule, and the rule is DRC. View BugCrunch report #251.

System Components: Schematic System

5533	A bug that allowed non-zero rotation of library components has been fixed. View BugCrunch report #223.

5694	An access violation due to negative font sizes in imported schematics has been fixed.
5730	Text rendering in exported DXF and DWG files has been fixed.

System Components: Soft Design Support

5254	An positional association in a port map of a component that is not declared may crash the vhdl parser. This is fixed. View BugCrunch report #528.

Importers and Exporters: Exporter - AutoCAD DWG

5535	AutoCAD DXF/DWG exports can now be included in output job files. View BugCrunch report #672.
5730	Text rendering in exported DXF and DWG files has been fixed. View BugCrunch report #1142.

Importers and Exporters: Exporter - DXF

5535	AutoCAD DXF/DWG exports can now be included in output job files. View BugCrunch report #672.

Importers and Exporters: Exporter - Hyperlynx

2634	The PCB Editor Save As... Export HyperLynx format utility now exports solid regions, polygon pours, and planes.
5514	Tiny arcs whose start and end points evaluate as coincident are now omitted, so that they are not mistaken for full circles.
5515	Layer and net names that contain special characters are now detected more consistently, and double-quotes are used as required.
5520	Rounded rectangle pad shapes are now correctly represented in the exported .hyp file.

Importers and Exporters: Importer - Allegro

5374	Bottom layer SMD pads are now imported correctly as simple mode padstacks.
5596	Polygon and filled shape objects residing on internal plane layers now import as split planes, and framing geometry is added to make them persistent.

Importers and Exporters: Importer - PADS

5187	Default plane and polygon relief connection rules have been added for pcb design import to more properly coincide with PADS global default thermal settings.
5525	Application of pad offset has been corrected and improved.
5526	Processing of DIF_PAIR design rules of v9.3 have been corrected.
5548	Processing of DIF_PAIR definition now correctly creates routing width rules.

Output Generators: Output - NC-Drill

5683	Corrected problem where rotated holes in embedded board arrays would produce incorrect NC drill output. View BugCrunch report #1075.

Output Generators: Output - Gerber

5491	The outermost pads of a blind/buried via are now generated, regardless of the option "Include unconnected mid-layer pads". View BugCrunch report #650.

Output Generators: Output - STEP

5492	The STEP Outjob now supports variants. View BugCrunch report #192.